

NETL.001DV4


PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

 Applicants : Jayesh R. Bhakta et al.

Group Art Unit 2818

Appl. No. : 10/765,420

Filed : January 27, 2004

For : ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE

Examiner : Ly D. Pham

AMENDMENT AND RESPONSE TO AUGUST 19, 2004 OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the August 19, 2004 Office Action, Applicants request the Examiner to reconsider the above-identified patent application in view of the following amendments and remarks.

Amendments to the Claims: Begin on page 2 of this Response.

Remarks: Begin on page 7 of this Response.